

ChipTest Engineering

About ChipTest

- Established in 2005, ChipTest is an IC Test & Automation Company
- ChipTest has 2 Core BUs : Test Engineering and Wisteria
- Over 30+ Years of Test Engineering Business Experience
- ISO 9001:2015 Certified ; Operations in India, Singapore & Malaysia
- Cost effective Offshore & Onsite solutions, providing Turnkey Product & Services Support •
- Philosophy : Business Beyond Profit ; Long-Term Value-Added Partnership Business Model
- ChipTest is an associate of the Valingro Group that facilitates, develops & build Enterprises girded by Values

Vision

Exceed the fast emerging needs of our Customers by:

- Accelerating time-to-market thru continuous Innovation & high Quality \geq
- Providing unparalleled Service that is Versatile & Cost-effective \geq

Mission

Corporate Objectives

- Values
- Business Ethics defines us as a Company 1.
- Professionalism defines us as Individuals 2.
- Citizenship defines our contribution to society 3.
- Profit earnings that enable achieving other 4 objectives 1.
- 2. **Client Satisfaction**
- 3. Competence
- **Employee Satisfaction** 4.
- 5. Growth



"To consistently excel in Semiconductor Test Software and Hardware Solutions and exceed the fast emerging needs of our Customers through continuous innovation, high quality, accelerated, versatile & cost–effective services, along with increasing value addition to all stakeholders"

Quality Methodology

- Quality Controlled Process flow approach \geq
- Archival of Process records and Quality data
- **Closely monitored Corrective Action Systems**
- ISO 9001:2015 Certified





Company Profile



chiptest.net

Solutions Driven By Knowledge Consistently excel in Semiconductor IC Test Engineering & Hardware Products with associated Embedded Solutions for global Customers



Business Units (BU)

- 1. Test Engineering
- 2. Wisteria Hardware Products & Automation Solutions
- 3. Allied Services
- New Product Development

(1) Test Engineering BU

- Program Conversion from Legacy platforms
- Product Design documents / Datasheet Review
- Test Plan Derivation for New Silicon Products
- Vector Conversion Design to ATE Format
- Product Characterization & Correlation
- Engineering to Production Integration
- Test Time, Yield improvement
- Test Coverage improvement
- Manufacturing Support Services
- Product & Test Engineering
- Offshore Customer centric Test Centres
- Dedicated Teams, Fast Time to Market
- Customers–IDM, Fabless, Start-up, Subcon

(2) Wisteria - Hardware Products & Automation Solutions BU

- Design for High Speed Digital & Precision Analog needs
- Schematics Design Cadence CAPTURE, Mentor PADS LOGIC & ALTIUM
- Board Layout Design ALLEGRO, ALTIUM and PADS Layout
- Implementation of High-Speed Layout techniques
- ATE load board design techniques
- Blind and buried Via's Implementation
- BGA Routing Techniques
- Final QC and CAM Support



Automation

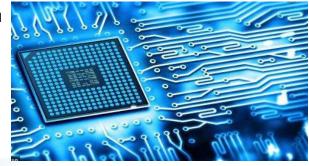
- Low Cost Tri-Temp Handlers
- Handler Conversion Kits & Test Sockets
- Pick & Place Solutions for Industries
- Poke yoke system implementation
- Process Control Monitoring Systems
- Auto Liquid Dispensing Systems



ATE & Bench Capabilities

- Advantest 93K
- Advantest T2000 IPSE & IPS and T65XX
- Teradyne ETS88, ETS364, ETS500, ETS800
- Teradyne J750, IFLEX
- LTXC ASL1K
- Bench Power Supplies, Oscilloscopes,
- V/I Meters, Thermo stream & Others
- Offline ATE Simulators
- Remote Login Access

PCB Capabilities



- OEE Dash boards of inline Equipment's
- Auto Screw Feeding System
- Auto Mounting & Inspection Systems
- Automotive Functional Test Systems
- Health Care Automation
- Food Products line Automation

chiptest.net

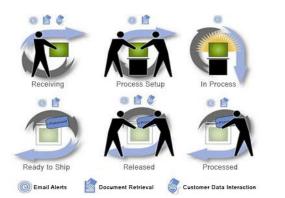




(3) Allied Services

- Wafer Sort & Final Package Level Production Testing
- Proto-Type Samples Packaging & Small Volume production
- New Product Reliability Qualification
- ESD/Latch-up Qualification
- > ATE Equipment Installation & Maintenance Support





 \triangleright

Delivery Methods

- Well defined Process Flow documents & Checklists
- ISO Work Procedures and Standards
- Setting & Monitoring of Key Performance Indices
- Project Scheduling & Milestone Tracking
- Systems for Continuous Process Improvement
- Customer specific Qualification Process

Customers

Device & Equipment Manufacturers

Global Semiconductor Customer base

- > Markets Served America, Asia Pacific & India
- Markets Explored Europe, Japan
- Computing, Mobile & Consumer Applications
- Customer specific Dedicated Teams
- Inbuilt Long-term Partnership Model





Infrastructure

- 10K sq.ft Facility with fully furnished Office space
- Well Equipped Static Dissipative Flooring with ESD Controls
- Temperature & Humidity Controlled Environment
- Long term Partners for Supply Chain Management
- High Speed Internet Connectivity with Remote Accessibility

Logistics

- Green Channel Status for Imports & Exports
- Zero Duty with Expedited Customs Clearance
- Convenient Logistics for Equipment Consignment
- Daily Connecting Flights to all Countries



chiptest.net





Professional Team

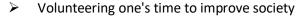
- Well Experienced Leadership
- > 50+ Years Total Test Engineer Experience
- Qualified & Skilled Test Personnel
- Proficiency in Communicating in English
- Ready availability of Resources for expansion

Human Resources

- Recruitment of Talented Engineers from Reputed Institutions
- Continuous & Standardized Training Methodologies
- Periodical Performance Appraisal Review
- Employee Appreciation & Rewarding Schemes
- Performance based Long Term Career Growth Plans



Socio-Economic-Contribution (S-E-C)



- Phase 1: < 35 Years of Age Focus on Career Building & Establishment</p>
- Phase 2: 35 to 65 Years of Age Ability to contribute to Economic Development
- Phase 3: >65 Years of Age Ability to contribute to Social Causes
- S-E-C is to contribute socially in Phase 2 along with Economic Development
- > 3 Areas identified to assist us in achieve this
 - Following discipline in any and everything we do
 - o Providing a helping hand to people around us in any way we possibly can
 - Showing the right path forward to people around us

Value Proposition

- Cost Effective One Stop Turnkey Test Solutions
- Customer-wise Dedicated & Talented Team
- Unparalleled 24x7 Service throughout the year
- Proven Track Record with focus on long term values
- Cutting edge Hardware & Embedded Technologies
- Enhancing the Time to Market for new Products
- > Closely held partnership model with all Stakeholders



ChipTest Engineering Private Limited

No.1/44, Mount Poonamalle High Road St.Thomas Mount, Chennai, Tamilnadu, India - 600 016 101 Cecil Street, #23-6 Tong Eng Building, Singapore, 069 533

ChipTest Engineering Tech Sdn Bhd

Suite 1705A Level 17 Menara Landmark, 12 Jalan Ngee Heng, 80000, Johor Bahru, Johor, Malaysia

eMail : info@chipte